KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE
(An Autonomous Institute under Kakatiya University, Warangal)
Opp : Yerragattugutta, Bheemaram (V), Hasanparthy (Mandal), WARANGAL - 506 015, Telangana State, INDIA.
काकतीय प्रौद्योगिकी एवं विज्ञान संस्थान, वरंगल - ५०६ ०१५ तेलंगाना राज्य, भारत
కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - 506 015, తెలంగాణ రాష్ట్రం, భారతదేశము
Office of the Controller of Examinations

No.   /MSE-II Schedule/B.Tech. III-Sem (URR-18R22)/2024/ 516 a

Date: 28-10-2024

MID SEMESTER EXAMINATIONS-II (MSE-II) FOR B.TECH. III-SEMESTER (URR-18R22), NOVEMBER-2024

TIME – TABLE   Examination Timings: FN 10.00 am to 12.00 pm

Date & Day of Examination	CE	ME	CSE	EEE	ECE	IT	CSE(N)	ECI	CSE(IoT)	CSE(AIML)	CSE (DS)
11.11.2024 Monday	Engineering Mathematics-III	Engineering Mathematics - III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III
12.11.2024 Tuesday	OE-I Strength of Materials	Mechanics of Solids	Object Oriented Programming through JAVA	Electrical Measurements & Instrumentation	OE-I Object Oriented Programming	Operating Systems	Object Oriented Programming through JAVA	OE-I Object Oriented Programming	Object Oriented Programming through JAVA	Object Oriented Programming through JAVA	Object Oriented Programming through JAVA
13.11.2024 Wednesday	Fluid Mechanics	Material Science and Metallurgy	Discrete Mathematics	Electromagnetic Fields	Signals & Systems	Discrete Mathematics	Operating Systems	Electronic Measurements and Sensors	Fundamentals of Internet of Things	Operating Systems	Operating Systems
14.11.2024 Thursday	Surveying	Engineering Thermodynamics	Computer Architecture and Organization	Network Theory	Network Analysis	Computer Architecture and Organization	Computer Architecture and Organization	Signals Systems and Random Processes	Computer Networks	Computer Organization and Architecture	Computer Organization and Architecture
19.11.2024 Tuesday	Construction Materials	Machine Drawing	Advanced Data Structures	Electronic Devices and Circuits	Analog Circuits – I	Advanced Data Structures	Advanced Data Structures	Electronic Devices and Applications	Advanced Data Structures	Advanced Data Structures	Advanced Data Structures
20.11.2024 Wednesday	--	--	Digital Electronics	--	Switching Theory & Logic Design	Switching Theory and Logic Design	Digital Electronics	Digital Circuits and Logic Design	--	Formal Languages and Automata Theory	Formal Languages and Automata Theory
21.11.2024 Thursday	--	Essence of Indian Traditional Knowledge	Essence of Indian Traditional Knowledge	--	--	Essence of Indian Traditional Knowledge	Essence of Indian Traditional Knowledge	--	Essence of Indian Traditional Knowledge	--	--

Students **with ID card** only permitted & should occupy his/her seat at least **5 minutes before** commencement of the examination.

Controller of Examinations